REMARKS

Applicant is in receipt of the Office Action mailed September 16, 2003. Claims 1-15 are pending in the application.

§ 103 Rejections

Claims 1, 3, 5, 10-15 were rejected under 35 U.S.C. 103(a) as being unpatentable over Jensen et al (US Patent 5,293,597) in view of Allegrucci et al (US 5,428,779). Applicant respectfully traverses these rejections based on the following reasoning.

Jensen et al. disclose a CONCURRENT CONTEXT MEMORY MANAGEMENT UNIT. Jensen et al teach and disclose three processes including a *process A* **copying data from a *process B* to *process C***, "As shown in FIG. 2, there are three processes, 201, 202 and 203 (A, B and C). Process A is a privileged process, and processes B and C are normal (unprivileged) processes. Processes B and C request that process A copy a block of data from process B (L bytes of data starting at virtual address VB) to process C (starting at address VC)" (*Emphasis Added*) (Col. 3, lines 50-56.

Allegrucci et al. disclose a SYSTEM AND METHOD FOR SUPPORTING CONTEXT SWITCHING WITHIN A MULTIPROCESSOR SYSTEM HAVING FUNCTIONAL BLOCKS THAT GENERATE STATE PROGRAMS WITH CODED REGISTER LOAD INSTRUCTIONS. Allegrucci et al. disclose that their invention relates to switching between two more programs by a multitasking system and method. "Multitasking is becoming more popular because the PC is becoming involved in more aspects of its operator's business, and the operator is requiring the PC to perform multiple tasks simultaneously. For example, the operator may like to have multiple documents and spreadsheets visible at the same time, while the machine handles electronic mail in the background, and continues to print the latest report. A multitasking operating system provides the best framework for the implementation of this level of functionality" (*Emphasis Added*) (Col. 1, line 61 through Col. 2, line 2). Allegrucci et al. also disclose, "The multitasking control software (sometimes called 'system software'), interrupts the execution of a current task to allow a new task to be processed, a previous task to complete, or to resume processing. The contents of the processor internal registers describing its current state for the current task are saved in memory by what is called a

context save operation. The processor register contents are replaced by the state of the new task to be processed by executing a context restore operation. Resuming the execution of a task is preceded by restoring the state the processor was in prior to the interruption of that task" (*Emphasis Added*) (Col. 2, lines 6-10). As Allegrucci et al. describe, context switching occurs when interrupting or restoring a task.

In contrast, Applicant's invention as recited in claim 1:

> Method for the direct call of a target function by means of a start function through a processor with a memory management unit (MMU) in a computer operated by an operating system, characterized in that the start function is a component of a first task with a first memory context and the target function is in another memory context and in that the first task performs a context switch from the first memory context into the other memory context, the target function is executed, and this context switch is reversed after the execution of the target function.

In further contrast, Applicant's invention as recited in claim 13:

> A method for the direct call of a target function by means of a start function in a computer system comprising a processor with a memory management unit (MMU), wherein the computer system includes an operating system, wherein the start function is a component of a first task with a first memory context and the target function is in another memory context, the method comprising:
>
> the first task performing a context switch from the first memory context into the other memory context;
>
> executing the target function in the other memory context;
>
> reversing said context switch to return to the first memory context after executing the target function.

Neither Jensen et al. nor Allegrucci et al. disclose the features of claim 1 or claim 13.

Applicant respectfully submits that the cited art must fairly teach or suggest to one to make the specific combination as claimed. In particular, the cited references do not teach or suggest, ". . . the direct call of a target function by means of a start function. . ." as recited in claims 1 and 13. As noted above, Allegrucci et al. teaches a system and method that interrupts the execution of a current task to allow a new task to be processed. In contradistinction, claims 1 and 13 disclose directly calling a target (second) function by a start (first) function. Contrarily to directly calling a target (second) function by a start (first) function as claims 1 and 13 disclose, Jensen et al. teaches and discloses a method to copy a block of data. Applicant, therefore, submits that claim 1 and claim 13 are patentably distinguished over both Jensen et al. and Allegrucci et al., taken both singly and in combination.

Applicants also respectfully submits that numerous ones of the dependent claims recited further distinctions over the cited art. However, since the independent claims 1 and 13 have been shown to be patentably distinct, a further discussion of the dependent claims is not necessary at this time.

CONCLUSION

Applicant submits the application is in condition for allowance, and an early notice to that effect is requested.

If any extensions of time (under 37 C.F.R. § 1.136) are necessary to prevent the above referenced application(s) from becoming abandoned, Applicant(s) hereby petition for such extensions. If any fees are due, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel PC Deposit Account No. 50-1505/5150-45700/JCH.

Also enclosed herewith are the following items:

☒ Return Receipt Postcard

☐ Request for Approval of Drawing Changes

☐ Notice of Change of Address

☐ Check in the amount of $ for fees ( ).

☐ Other:

Respectfully submitted,

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